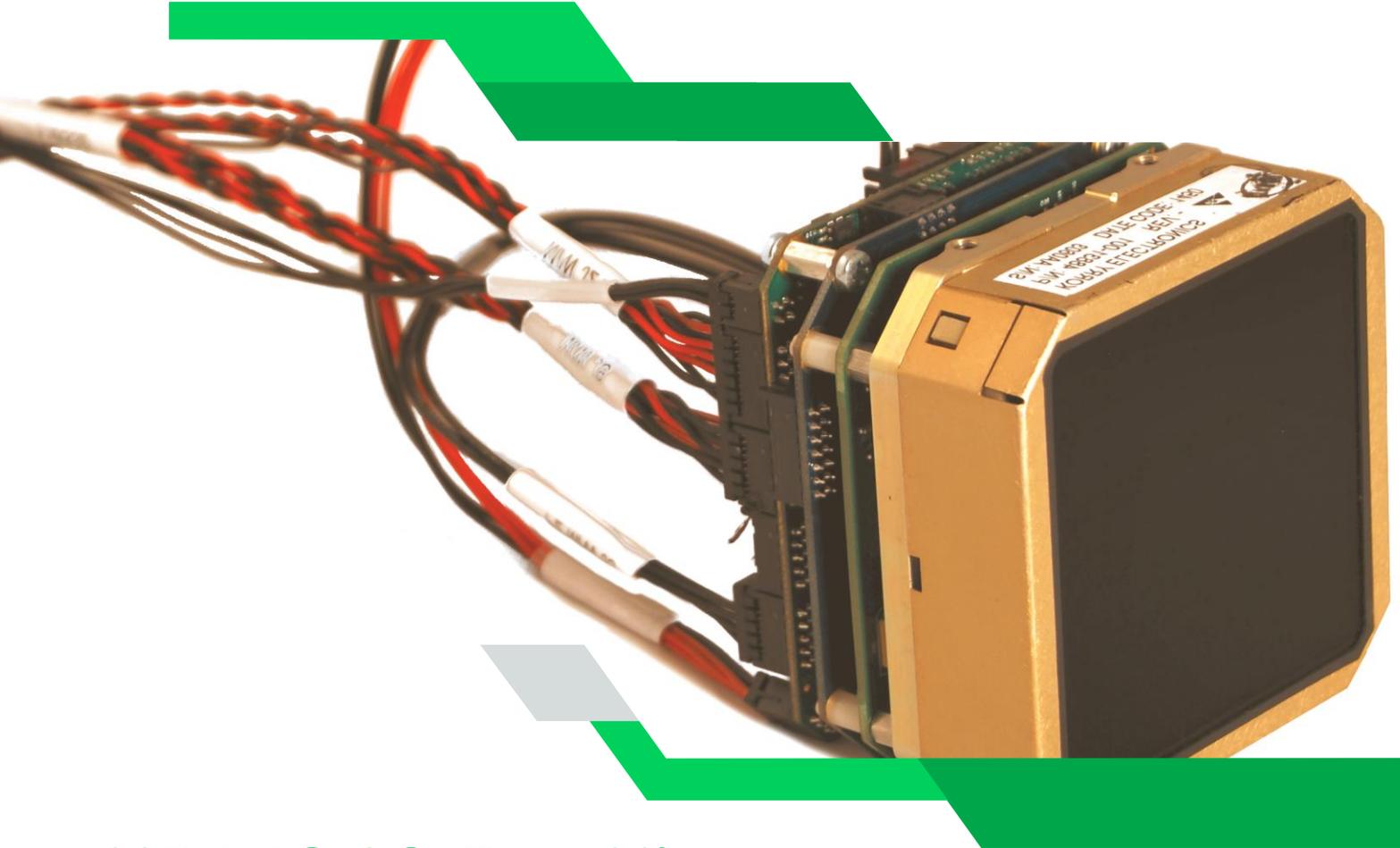




# Korry

*Illuminating. Always.*



## KDM340 DevKit

### Development Kit with VP7 Video Processing Module

Integration with a VP7-3ATI video processing module makes interfacing with a Korrry KDM340 display a breeze, providing a versatile, simple-to-use development kit for simulators and other non-flight applications.

Since the KDM340 only accepts parallel RGB video, the VP7-3ATI processing module adapts analog video or TMDS digital video from a standard PC and

lets you display a user-defined window directly on your KDM340 unit.

The KDM340 DevKit supports windowing, frame rate conversion, resizing and color conversions and accepts TMDS or analog as standard inputs.

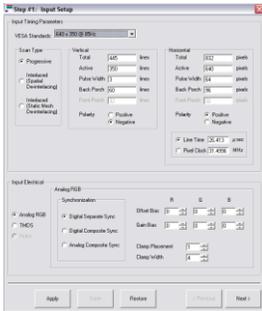
Adjustments and reconfigurations can be made in the field via utility software.



# VP7 Configurations Software

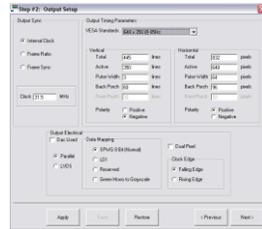
The VP7-3ATI video processing module comes with a VP7 configuration utility that runs on Windows platforms that connect through a RS-232 serial cable. This software uses a 4-step process to set up the VP7-3ATI for your application.

### Step 1: Input timing



Sets up the input timing and electrical definitions

### Step 2: Output timing



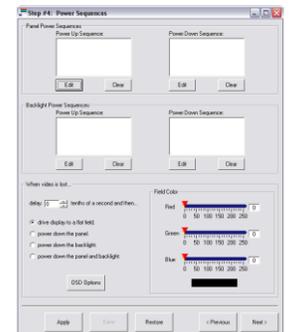
Sets up the output timing and electrical definitions

### Step 3: Windowing and scaling



The areas of interest within the input image and the mapping to the output resolution, thereby defining windowing and scaling functions

### Step 4: Power sequence



Sets up the power and video sequencing to the KDM340 display

# KM340 DevKit Configurations

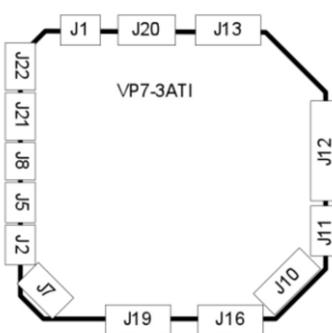
The KDM340 with VP7-3ATI comes as a standard configuration with both analog and DVI inputs. Development kits are available in the following configurations without a heater

Brightness	NVIS Compatibility	KDM340 P/N	P/N (with VP7)
150fL	Non-NVIS	39390-001	48831-001
150fL	NVIS	39389-001	48831-002
200fL	Non-NVIS	39388-001	48831-003
200fL	NVIS	39387-001	48831-004

# Capabilities

Based on state-of-the-art image processing technology, VP7-3ATI capabilities include:

- Digitization of computer-generated video sources with separate syncs or sync-on-green
- Non-interlaced and interlaced RGB inputs and outputs
- Digitization and de-interlacing of consumer video formats, including RS-343 formats
- Frame rate conversion
- Independent horizontal and vertical scaling
- Programmable image position within larger background area for both input and output
- Incoming video gain and offset adjustments
- Programmable power sequencing to panel
- Fine-phase clock adjustment for pixel sampling
- Image reversible left to right and top to bottom
- Remote interface for both set-up and operational control
- Same fit as KDM340 envelope X and Y, with overall depth increase to 2.23 inches



- J1 8-pin Hirose DF11 for discrete in and contrast
- J2 10-pin Hirose DF11 for FPGA configuration
- J5 10-pin Hirose DF11 for RS-232 control
- J7 6-pin Hirose DF11 for power input
- J8 10-pin Hirose DF11 for input analog video
- J10 16-pin Hirose DF11 for discrete I/O to display
- J11 10-pin Hirose DF11 for control to display
- J12 32-pin Hirose DF11 for digital data to display
- J13 14-pin Hirose DF11 for LVDS output
- J16 14-pin Hirose DF11 for backlight inverter control
- J19 8-pin Hirose DF11 for BIOS table select
- J20 8-pin Hirose DF11 for external LED connection
- J21 12-pin Hirose DF11 for TMD5 input
- J22 4-pin Hirose DF11 for EDID interface



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The information and data given are typical for the equipment described. However, any individual item is subject to change without any notice